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Sc 2 2 2005

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant:

Suresh Balasubramanian, et al.

Docket Number: TI-36687

Serial No.: 10/768,131

Art Unit: 2824

Filed: 02/02/04

Examiner: Eric J. Wendler

For:

Tracking Circuit Enabling Quick/Accurate Retrieval of Data

Stores in a Memory Array

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Karen Vertz

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X FACSIMILE COVER SHEET  NEW APPLICATION DECLARATION ASSIGNMENT FORMAL DRAWINGS X INFORMAL DRAWINGS - 3 pages CONTINUATION APP'N DIVISIONAL APP'N		X AMENDMENT 111 (12 pages)  EOT  NOTICE OF APPEAL  APPEAL (# Pages)  ISSUE FEE & PUBLICATION FEE (1 Page)  REPLY BRIEF (IN TRIPLICATE) (# Pages)
NAME OF INVENTOR(S):  Suresh Balasubramanian, et al.  TITLE OF INVENTION: Tracking Circuit Enabling Quick/Accurate Retrieval of Data Stores in a Memory Array		Serial No.: 10/768,131 Filing Date: 02/02/04
TIFILE NO.:  TI-36687  FAXED: 9-22-05  DUE: 11-15-05  ATTY/SECY: AKS/kly	DEPOSIT ACCT. NO.: 20-0568	<b>-</b>

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For: TRACKING CIRCUIT ENABLING QUICK/ACCURATE RETRIEVAL OF DATA

STORED IN A MEMORY ARRAY

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

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### **AMENDMENT**

In response to the Official Action in this case mailed August 15, 2005, please enter the following: